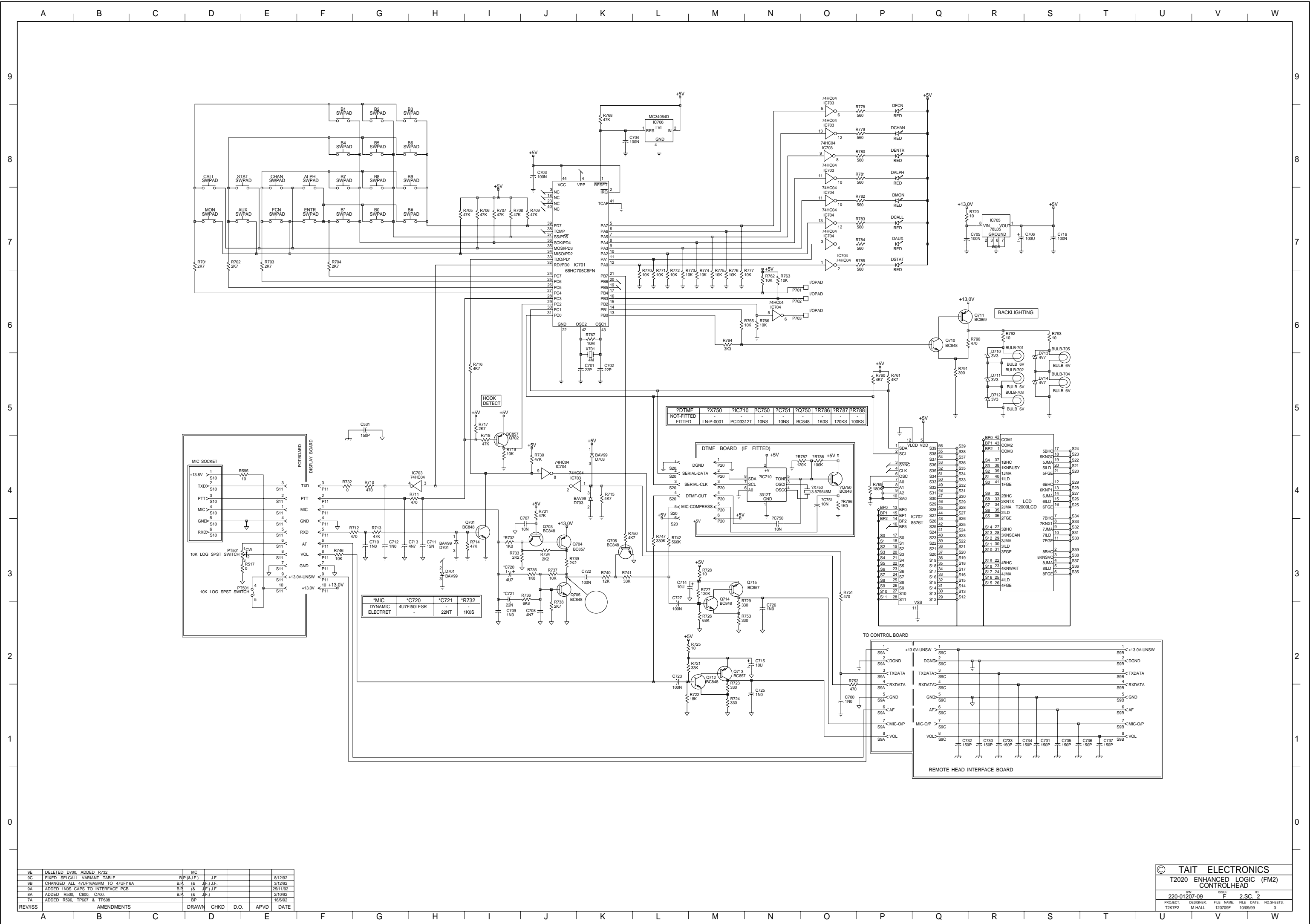
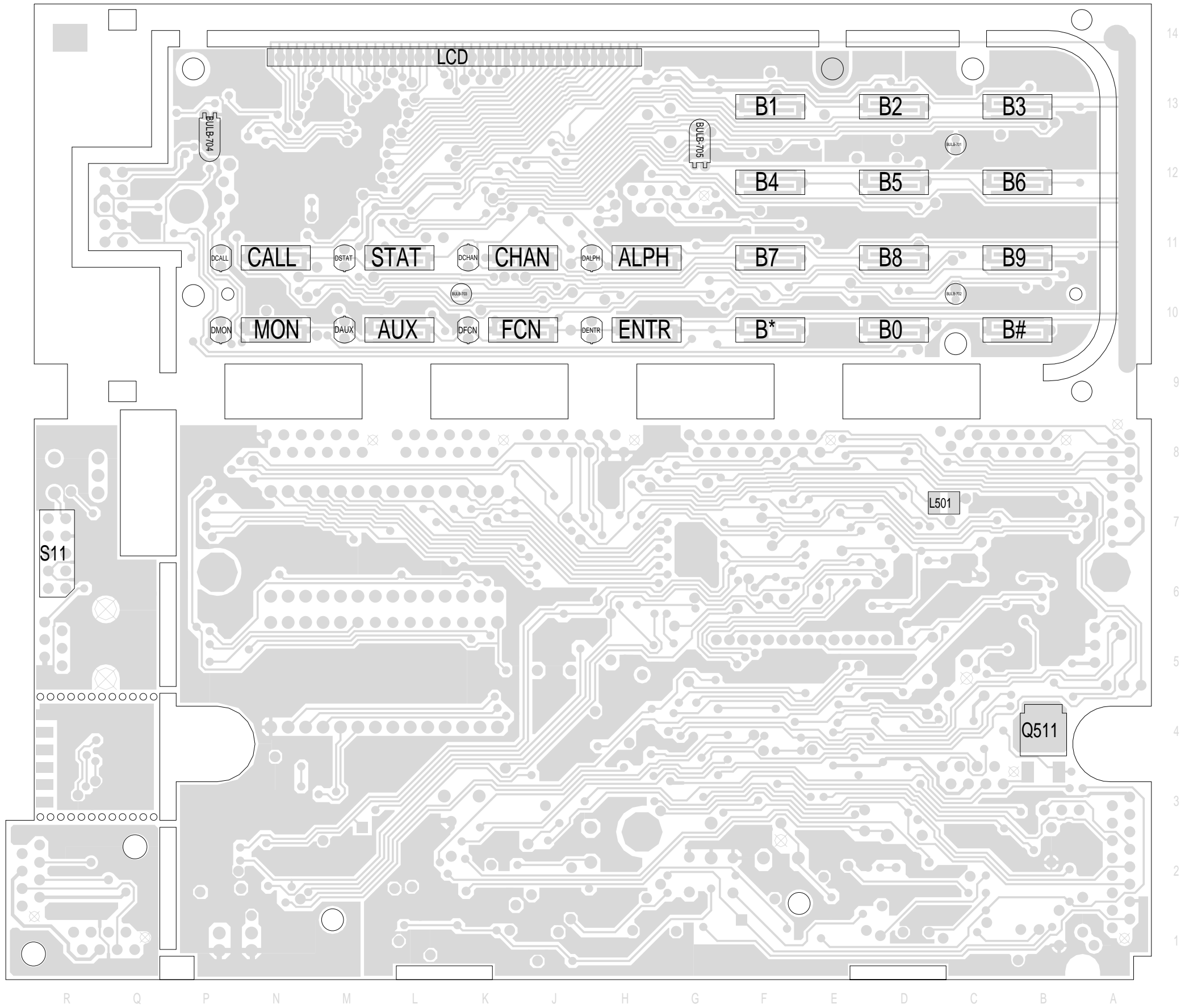


REV/ISS	DESCRIPTION	DRAWN	CHKD	D.O.	APVD	DATE
9E	DELETED D700, ADDED R732					8/12/92
9C	FIXED SELCALL VARIANT TABLE					3/12/92
9B	CHANGED ALL 47UF168MM TO 47UF16A					25/11/92
9A	ADDED 1N53 CAPS TO INTERFACE PCB					2/10/92
8A	ADDED R500, C600, C700					16/6/92
7A	ADDED R596, TP607 & TP608					

© TAIT ELECTRONICS
 T2020 ENHANCED LOGIC (FM2)
 CONTROL BOARD
 220-01207-09
 PROJECT: T2020-09 FILE NAME: T2020-09.DWG FILE DATE: 10/09/99 NO SHEETS: 3



REV/ISS	AMENDMENTS	DRAWN	CHKD	D.O.	APVD	DATE
9E	DELETED D700, ADDED R732					8/12/92
9C	FIXED SELCALL VARIANT TABLE	B.P.(J.F.)	J.F.			3/12/92
9B	CHANGED ALL 47UF168MM TO 47UF16A	B.P. (& J.F.)	J.F.			25/11/92
9A	ADDED 1N55 CAPS TO INTERFACE PCB	B.P. (& J.F.)	J.F.			2/10/92
8A	ADDED R500, C600, C700	B.P. (& J.F.)	J.F.			16/6/92
7A	ADDED R596, TP607 & TP608	BP				



TAIT ELECTRONICS	IPN:	ISS:	ID:	DATE:
	220-01207-09	9	2.BA	10 Aug 1999
T2000-FM2 PCB LAYOUT - BOTTOM SIDE				

Scale: 1.6:1 ; Rotation: 0 degrees